Introduction

μTasker is an operating system designed especially for embedded applications where a tight control over resources is desired along with a high level of user comfort to produce efficient and highly deterministic code.

The operating system is integrated with TCP/IP stack and important embedded Internet services along side device drivers and system specific project resources.

μTasker and its environment are essentially not hardware specific and can thus be moved between processor platforms with great ease and efficiency.

However the μ Tasker project setups are very hardware specific since they offer an optimal pre-defined (or a choice of pre-defined) configurations, taking it out of the league of "board support packages (BSP)" to a complete "project support package (PSP)", a feature enabling projects to be greatly accelerated.

Very often there is a requirement for software updates in the field and where possible over the Internet. There are several methods which can be used, each with its advantages and disadvantages. The $\mu Tasker$ boot-loader support is optional in the $\mu Tasker$ and chooses a technique to allow Internet enabled uploaded of application software based on several IP techniques (TFTP, FTP, HTTP POST etc.) as well as optional serial methods using an absolute minimum of boot software space. It allows complete uploads of application software including operation system, driver, interrupt and TCP/IP stack code for maximum flexibility but also with reliability as top priority; failed uploads will not result in disaster but can be repeated (or are automatically repeated) until successful.

μTasker boot-loader strategies

The μ Tasker boot-loader strategy can be configured depending on the project requirements. The following options are possible:

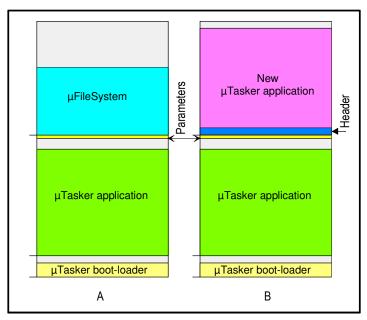
- TFTP upload support integrated in the boot loader (Ethernet, ARP and UDP)
- Serial loader support integrated in the boot loader
- Override options to force serial or TFTP uploads (eg. Input port state at reset)
- Override of IP configuration option (eg. Input port state at reset) to enable a board with unknown configuration to be contactable via Ethernet – useful mainly together with TFTP support
- μTasker "bare-minimum" loader

μTasker "bare-minimum" loader

The "*bare-minimum*" loader is the base for all other configurations. It supports safe updates of new application software which has been prepared by the previous application and as such has the advantage of requiring an absolute minimum amount of space in FLASH. However this solution can also be a very powerful solution since it still can enable the safe update of entire software, including unrestricted operating system, driver and TCP/IP stack patches, over the Internet.

The μ Tasker "*bare-minimum*" loader is a normal small standalone program. It doesn't have any of its own network capabilities and doesn't configure the Ethernet interface. It requires no further options to be enabled to achieve its goals.

The μ Tasker "*bare-minimum*" loader assumes that at start-up one of the following two states exists.



State A is the initial and normal condition. A μ Tasker application exists in FLASH at the application's start position and there is no new software waiting to be updated. Note that the case where only the μ Tasker boot-loader exists is not normally a valid case – the initial load (which will normally be performed using BDM) is loaded together with the first μ Tasker

application. [If the μ Tasker boot-loader contains also serial loading support, the application can of course be loaded in a second step].

When the μ Tasker boot-loader recognises state A it simply allows the μ Tasker application to start operating. Details about starting the application and the configuration of the application program are given later in this document.

State B exists thanks to the support built in to the μ Tasker application, which has allowed a new version of a μ Tasker application to be programmed into the region in FLASH reserved for it. This will usually be performed over the internet (network). It is not the boot loader which is responsible for loading this!

The new μ Tasker application includes also a small header which allows the μ Tasker bootloader to recognise it as a valid copy of code and to also verify its integrity. This is extremely important since the device will lose its IP uploading support if an unsuitable program were to be loaded in place of the original one.

It is also clear that both the old and new μ Tasker applications must fit into the available non-volatile space. This is due to the fact that one of the programs must at all times be intact in order to guaranty that the update can not fail and leave the device in an unusable state. It is not however absolutely necessary that this is in the internal FLASH. If additional serial FLASH is available this can be optionally supported. As long as the internal FLASH has adequate space, it is usually easy to justify using a device with more FLASH than the actually application needs alone due to the valuable additional feature that the IP uploading support represents.

It is also important to note that the space used to store the μ Tasker application may overlap with the μ Tasker file system used during normal operation. For example a M52235 with 256k FLASH can use a 128k file system for web pages and other data alongside the application code of up to 120k. A new μ Tasker application can be loaded to the file system area ready for programming. No additional FLASH would be required in this example, although the contents of the file system will usually also have to be restored (eg. via FTP) after the upload procedure is complete.

Procedure

The μTasker boot-loader is always the first program started after a reset.

It first checks to see whether there is a new μ Tasker application waiting to be programmed. To be recognised as a valid new μ Tasker application it must have a valid header and its integrity must be verified according to the header contents. The header is detailed later.

If there is no new application waiting, the normal application will be started. (If the μ Tasker boot-loaded supports a serial loading method it may also first check that the application is present or offer the serial download if it is not. It may also offer a download menu when a specific input port in held in a defined state at reset.)

Should a new and valid μ Tasker application we waiting to be programmed, the original application will be deleted. It should be noted that a restart of the device during this phase is not dangerous since the delete will be simply repeated.

After the delete phase has terminated the new μ Tasker application is copied to the application code space.

The new application is now verified according to the integrity check in the header, to be sure that there were no errors when copying.

Finally the copy of the µTasker application is deleted and the device is reset.

It can be shown that a power fail or other error can be tolerated at any part of the procedure and that the final reset is equivalent to a standard start with the μ Tasker application programmed.

Code Header

In order to ensure that the μ Tasker boot-loader doesn't attempt to program unsuitable code and also to guaranty the integrity of the loaded code a small header is used. The header consists of 8 additional bytes as follows:

```
unsigned long ulCodeLength;
unsigned short usMagicNumber;
unsigned short usCRC16;
```

The header can be added to the program code by using a simple utility program which accepts a normal binary file, calculates its check sum and adds these 8 bytes to the beginning of the file. This utility program is supplied with the μ Tasker project (in the tools directory) and is called uTaskerConvert.exe

The code length is then used to verify the size of the received code and also for the verification of its CRC value. The magic number is used as a version number and also as basic protection against malicious uploads of code otherwise respecting the format – the magic number can be set for a project and kept secret (although it could be eves dropped from the upload traffic).

The CRC is calculated over the complete code (without header) and used when the μ Tasker boot-loader checks for a valid waiting software and also when verifying that the newly loaded software has indeed been copied without errors. Only when the new μ Tasker application has been successfully tested against its CRC value will the backup version finally be deleted.

Setting up the µTasker project, preparing and uploading the file

The μ Tasker project requires the following small modification to be able to operate together with the "*bare-minimum*" loader.

Its start address must be set to correspond to the start address in the "bare-minimum" loader. This can be set in the linker file.

This is set up, for example, in the CodeWarrior .lcf file in the memory definition like this. In this case 0x800 (2k) has been left at the beginning of FLASH for a μ Tasker boot-loader is up to 2k. Also the length of the FLASH block has been reduced by the same amount

```
flash (RX) : ORIGIN = 0 \times 0000800, LENGTH = 0 \times 0003F800
```

The project is otherwise compatible, including the use of the interrupt vector table.

When the project is compiled a binary image should be created. This can usually be set up in the linker configuration of the project.

Finally the output file is converted to a format with the necessary header. Here is an example of calling the uTaskerConvert.exe utility to add a header with a magic number of 0x1234 to the input binary file uTasker_demo.bin

```
uTaskerConvert uTasker_demo uTasker_update.bin -0x1234
```

The output file $uTasker_update.bin$ can then be uploaded to the target using whatever method is supported by the presently loaded $\mu Tasker$ application code. The following shows a typical solution using the HTTP POST method which is demonstrated in the standard $\mu Tasker$ demo project assuming that the upload support is available and activated.



A standard Web Browser input allows the user to select a file to be loaded from the user's hard disc. In this example the user has already selected a file with the name sw1.bin - the format of which has been prepared to include the necessary header.

By clicking on the SW Update button the Browser sends this file using the HTTP POST method. The μ Tasker application software saves the received data to FLASH at the required location and commands a reset of the board. This starts the μ Tasker boot-loader update procedure as previously described, after which the new software will be started.

It is important to note that the uploaded code is either posted to a dedicated FLASH memory region or to within or overlapping the μ Tasker file system. It is not posted to the card parameter region so that the IP configuration of the board remains intact. If this were not respected it would be possible to lose user settings and should thus be avoided. The coordination of the posting itself is resolved in the μ Tasker application code.

μTasker boot-loader details for the Freescale[™] Coldfire M5223X

The details of the realisation are rather processor specific and so it is necessary to have a good understanding of the processor it is running on. So we must learn some specifics of the device to understand its restrictions and how and why the solution has been shaped for it.

When the Coldfire starts it automatically reads two long words from the exception vector table. The first is its initial stack pointer value and the second is the initial program counter value. Since the exception vector table is located at reset at the address 0x00000000, in FLASH in the M5223X, it is clear that the μ Tasker boot-loader must reside at the beginning of FLASH so that it can coordinate the initialisation of the system.

The initial program counter, read from FLASH location 0x00000004, is the start address of the μ Tasker loader code.

The exception vector table includes in total 256 vectors, including the SP and PC and it is typical to reserve 1k of space at the start of FLASH to contain the interrupt vector addresses used later by the code. Of course the μ Tasker loader has no idea where these will later be located once the user code has been installed and so the exception vector table is not used in FLASH but rather the code starts directly at the location 0x00000008, thus utilising the FLASH as efficiently as possible.

Note the following complication in the M5223X: The locations between the addresses 0x400 and 0x417 are read on reset and used to configure some FLASH control registers. Therefore the μ Tasker boot-loader sets up the compiler and linker to position zeros in these locations. If this is not done, the FLASH configuration can leave FLASH blocks with protection against instruction accesses and so to program failure.

The stack pointer is set to the top of internal SRAM, positioned by default between 0x20000000 and 0x2000ffff (32k in all M5223X devices). Therefore the content of the first FLASH locations are defined:

```
0x00000000: 0x20008000
0x00000004: 0x00000008
0x00000008: First instruction of μTasker boot-loader
...
...
0x000000400: 00000000
0x00000404: 00000000
0x00000408: 00000000
0x0000040c: 00000000
0x00000410: 00000000
0x00000418: Further μTasker boot-loader code
...
...
...
```

The "bare-minimum" loader requires less that 2k in the M5223X and so the application starts at 0x00000800 (the second FLASH sector).

Although the start address of the μ Tasker application has been moved from its usual starting address of 0x00000000 to 0x00000800, no further code changes are necessary. The μ Tasker boot-loader takes over the normal reset procedure of loading the stack pointer with the first long word and setting the PC with the second long word and so the reset vector retains its function. The μ Tasker project sets up the interrupt vectors at the start of SRAM, rather than leaving them in FLASH and so this is also compatible.

Conclusion

This document has described in detail the operation and use of the "bare-minimum" method as implemented by the μ Tasker boot-loader.

The advantage of this method is that the boot code occupies only one FLASH sector (2k) and still allows safe software uploads over the Internet. The document has also described the code size limitations of this method and will be extended in the next edition with further optional support of serial and TFTP loading methods as well as uploads to serial FLASH. Their realisation and operation will be explained, including their corresponding advantages and disadvantages.

Future editions will also include details of the implementation on other processor types supported by the μ Tasker project.